

FIG.1

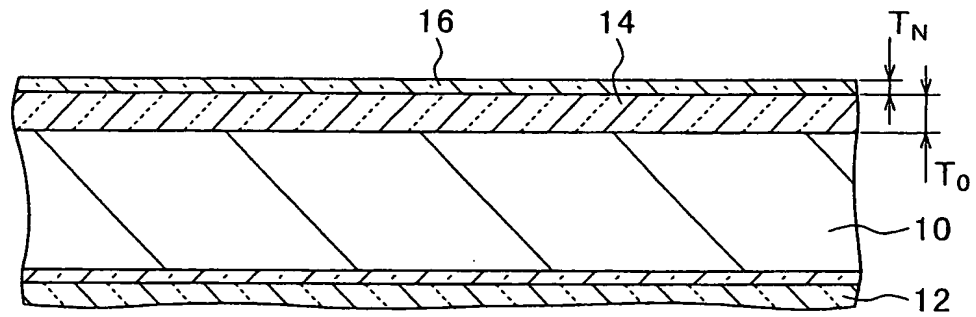
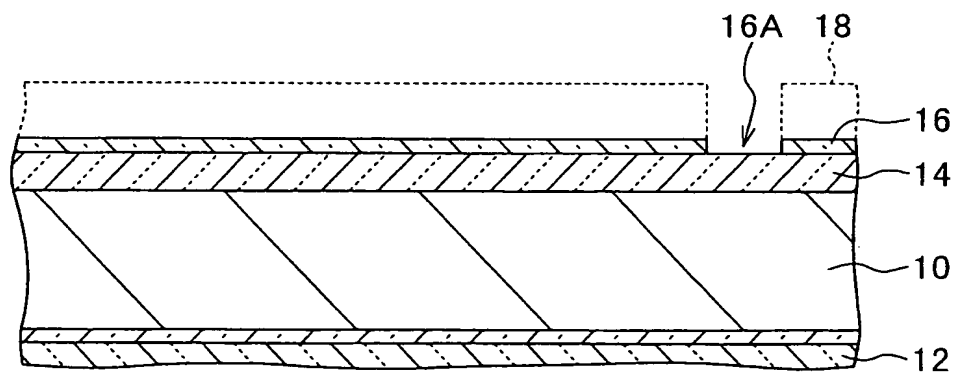


FIG.2



A cross-sectional view of a semiconductor device 22. The device features a substrate 10 with a bottom layer 12 and a top layer 14. A central region 16 is defined by a top surface 16A and a side surface 20. A dashed line 24 indicates a cross-section of the device.

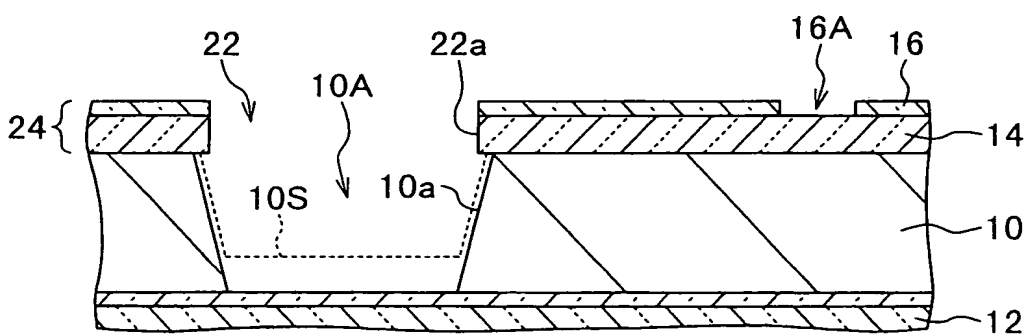
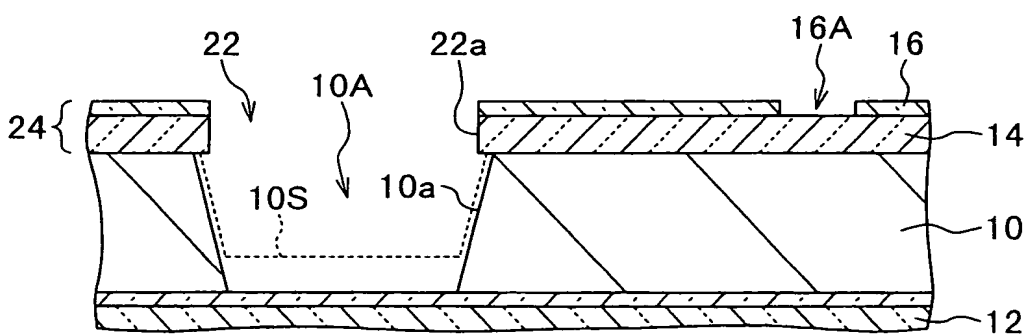


FIG.5

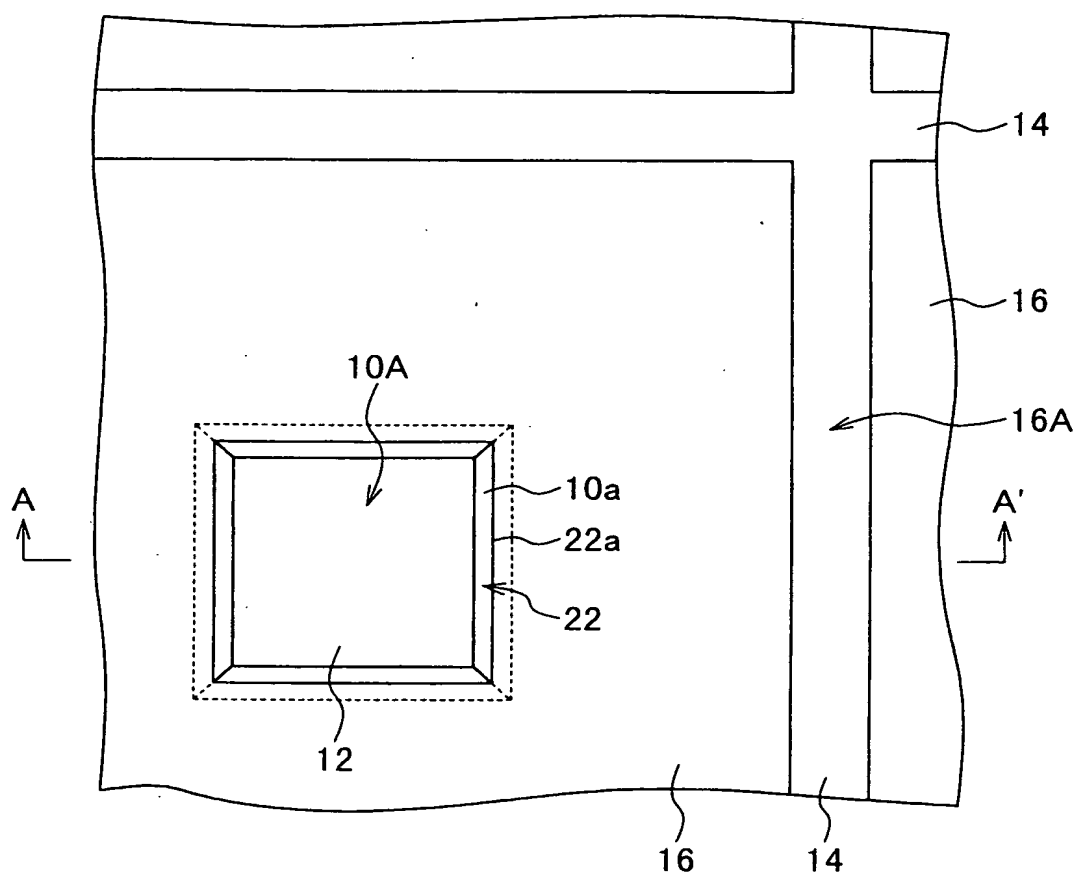


FIG.6

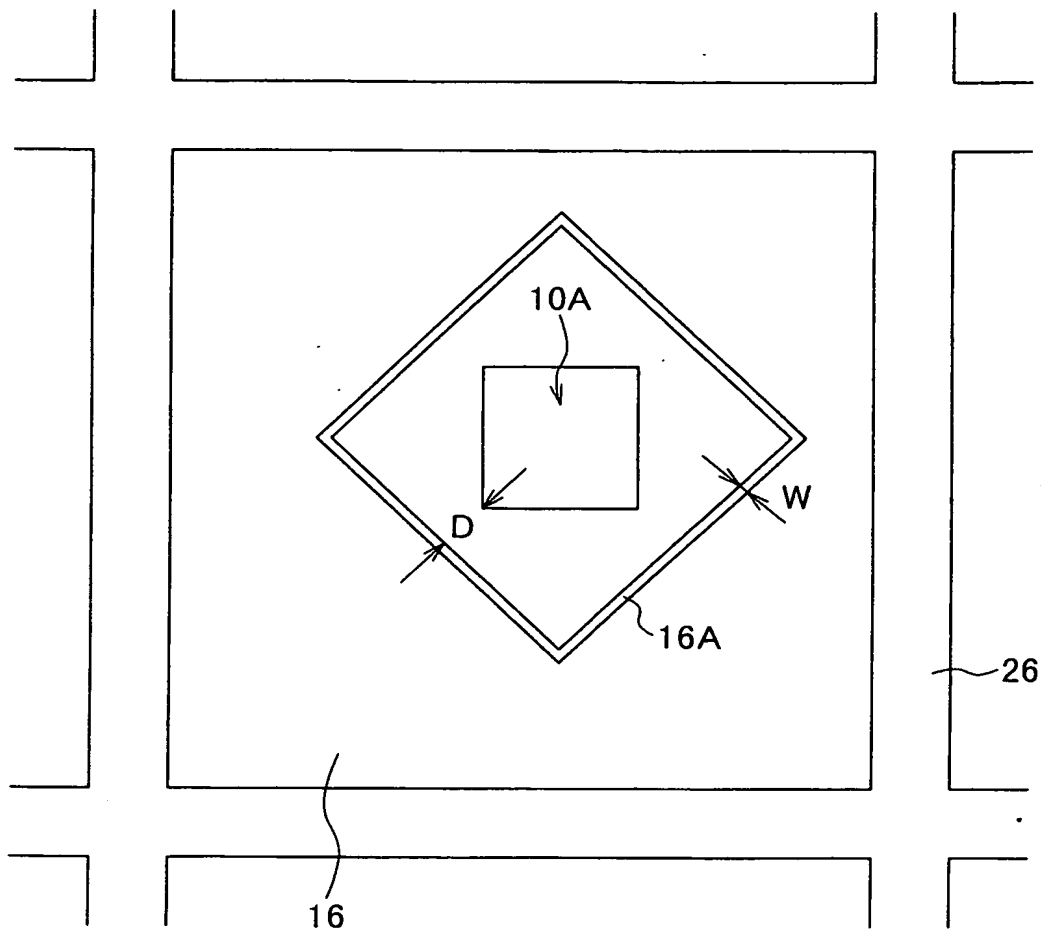


FIG.7

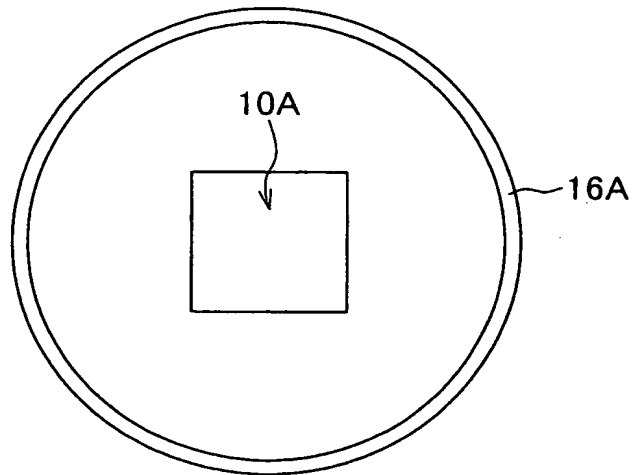


FIG.8

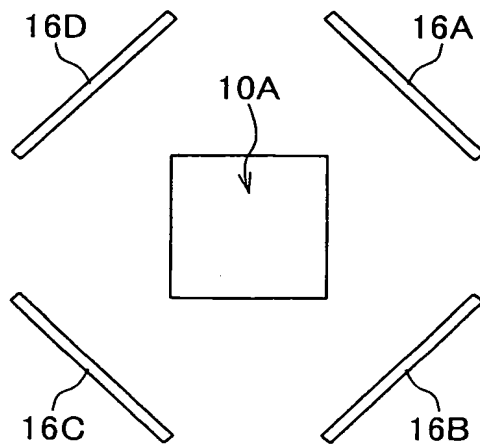


FIG.9

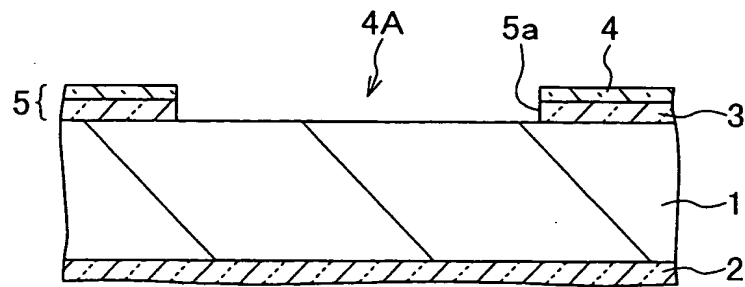


FIG. 10

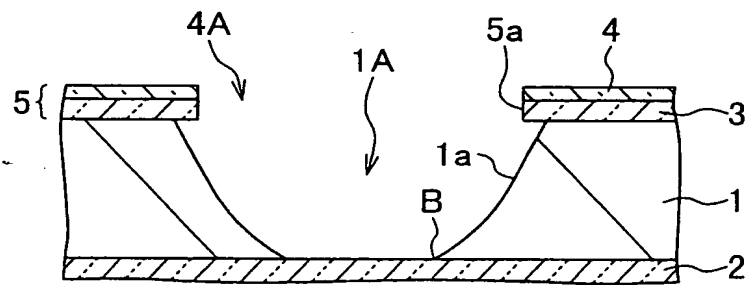


FIG11

